

FAST SYMBOL TIMING RECOVERY FOR TRANSMITTED DIGITAL DATA

Inventors: Yanbin Yu and Quingyi Zheng

Docket No.: U001 100014

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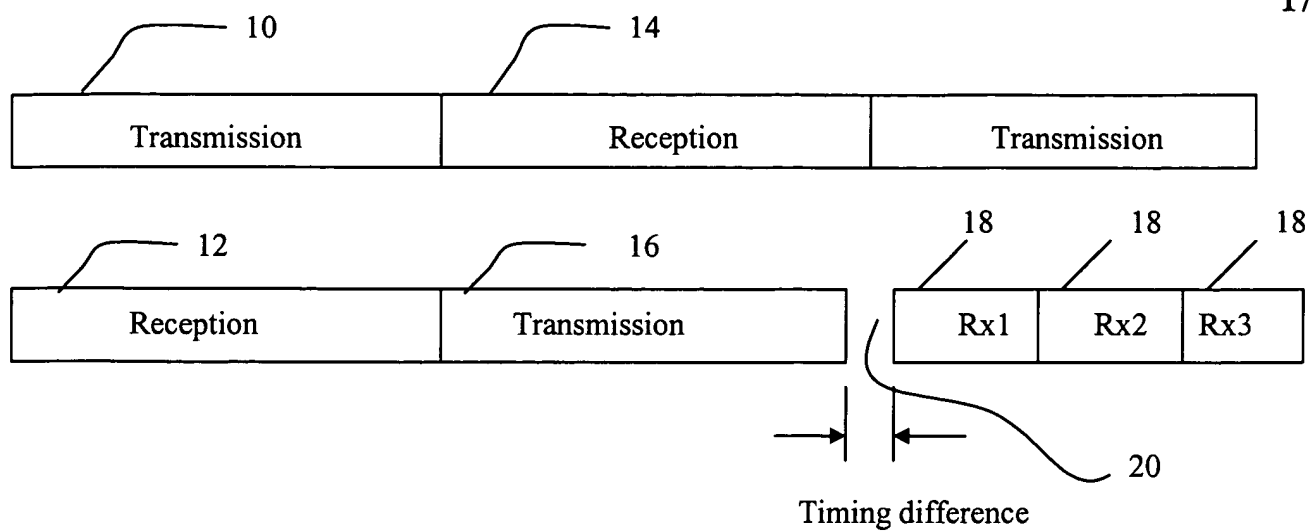


FIG. 1

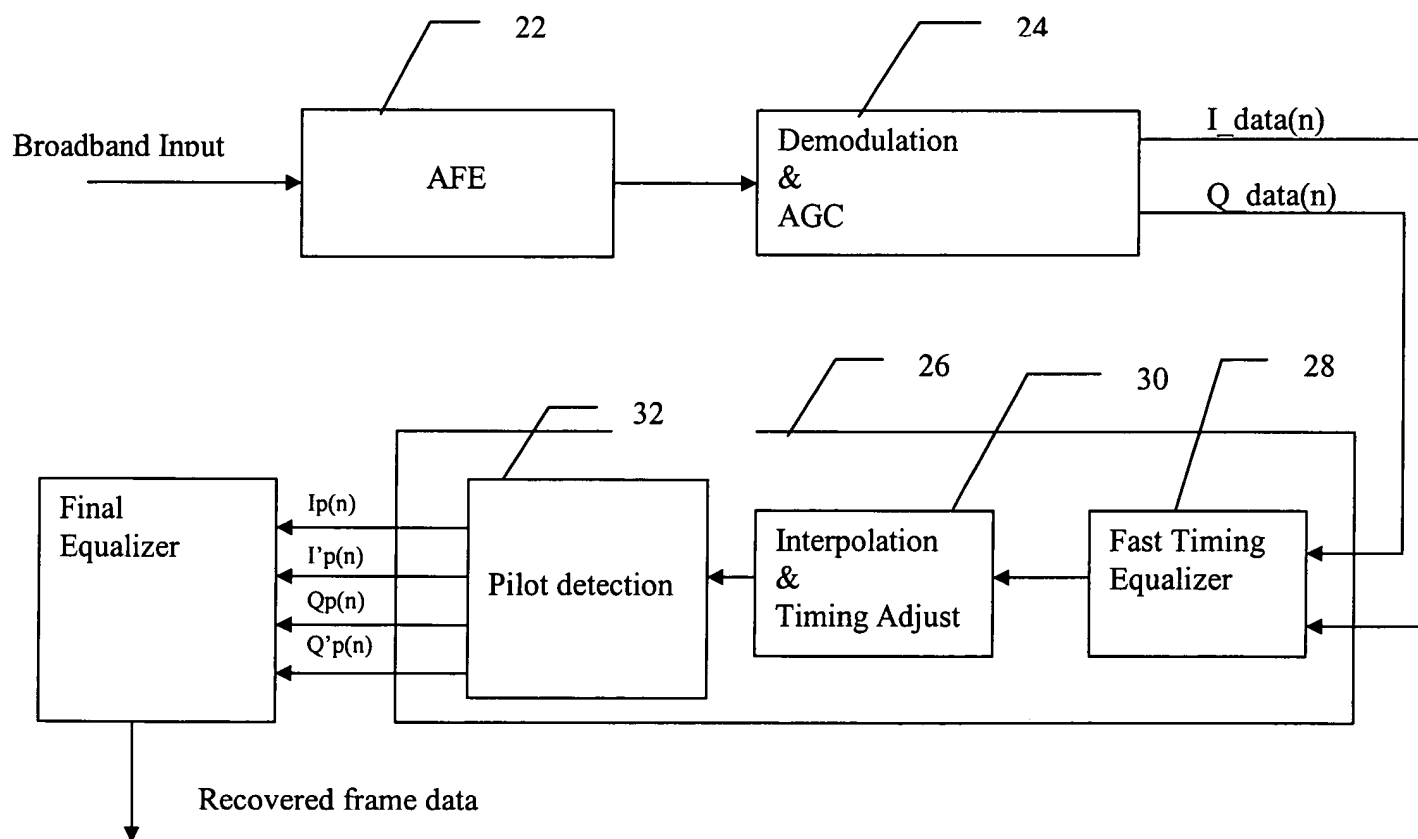


FIG. 2

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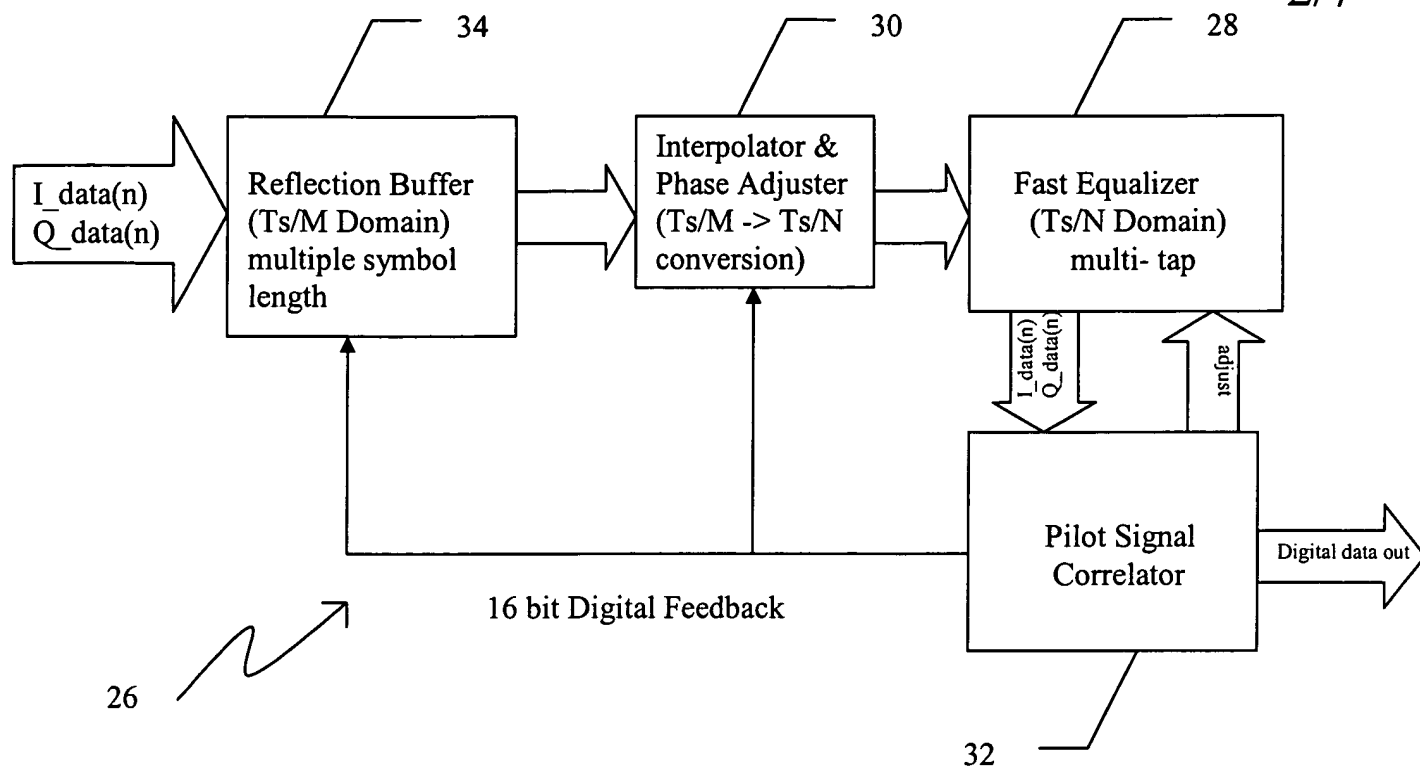


FIG. 3

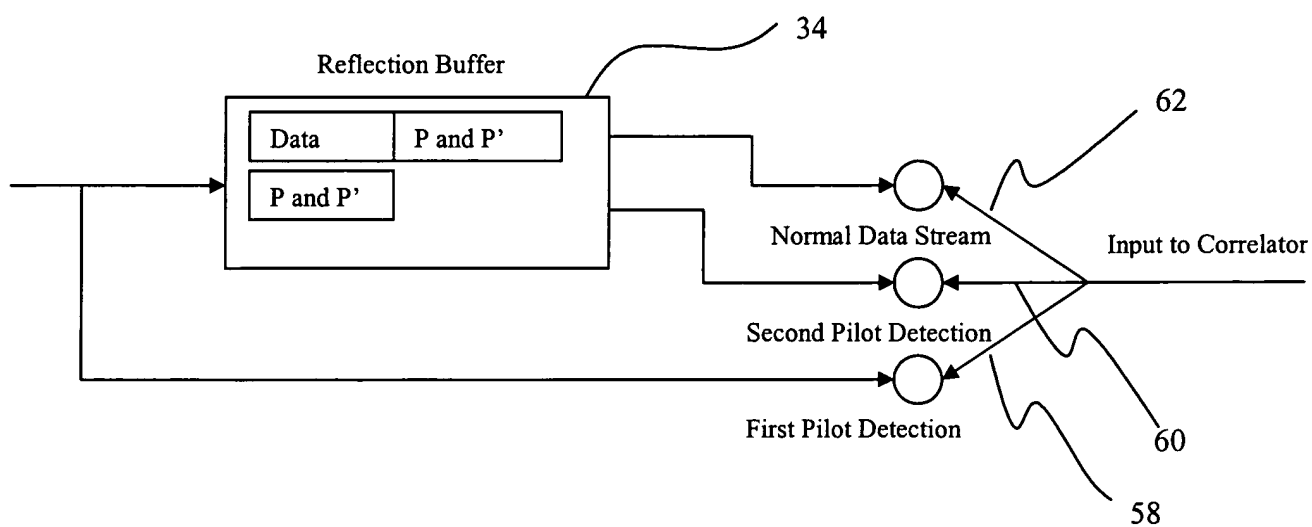


FIG. 5

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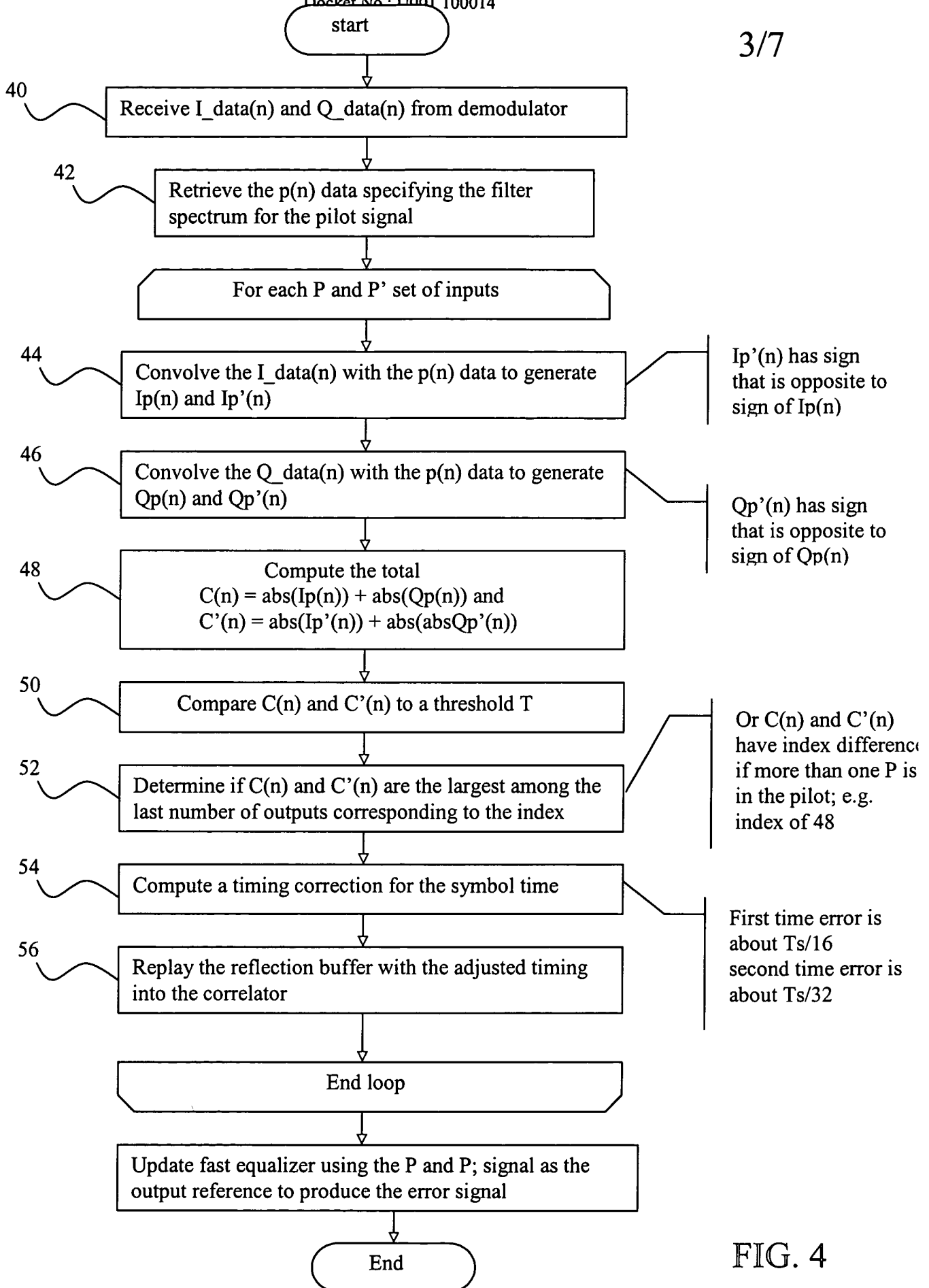


FIG. 4

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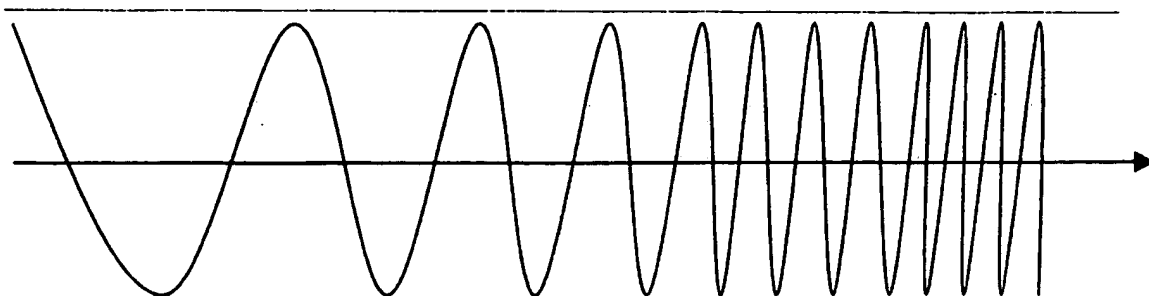


FIG. 6a

Polarity	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	1	1	1	1	0	0	0	1	1	0	1	0	1	0	1	0
1	0	0	0	0	1	1	1	0	0	1	0	1	0	1	0	1

FIG. 6b

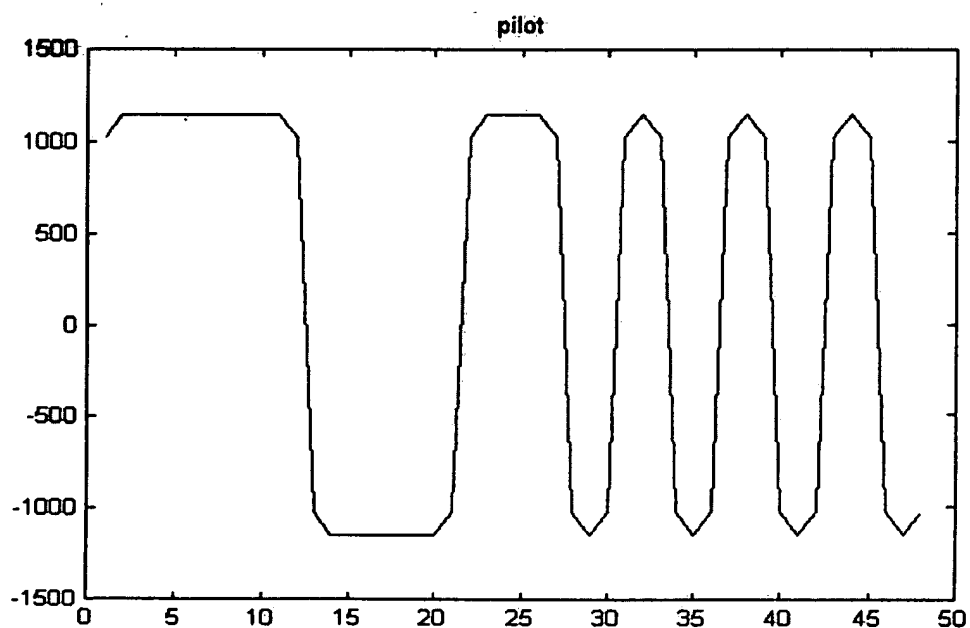


FIG. 6c

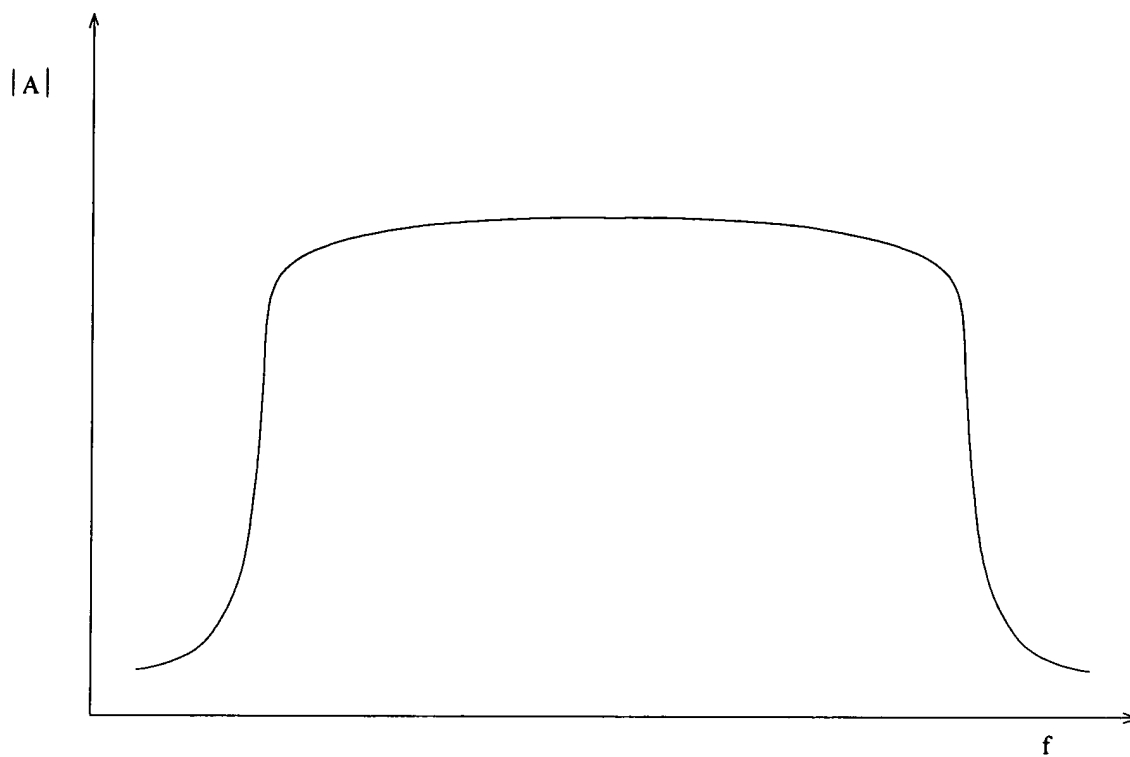


FIG. 6d

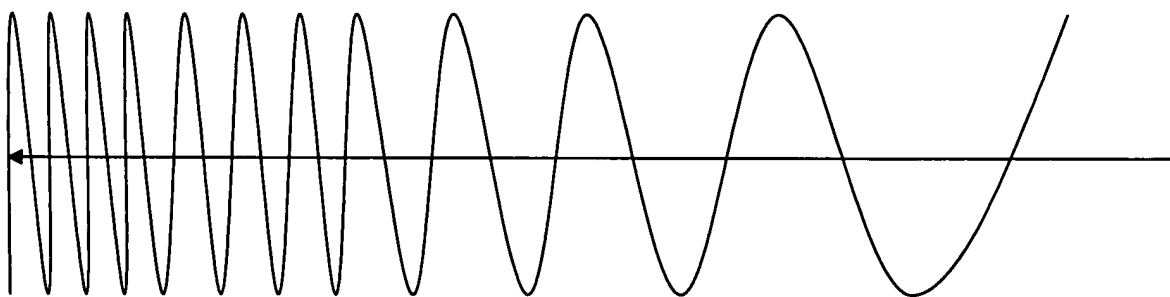


FIG. 6e

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Polarity	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	0	1	0	1	0	1	0	1	1	0	0	0	1	1	1	1
1	1	0	1	0	1	0	1	0	0	1	1	1	0	0	0	0

FIG. 6f

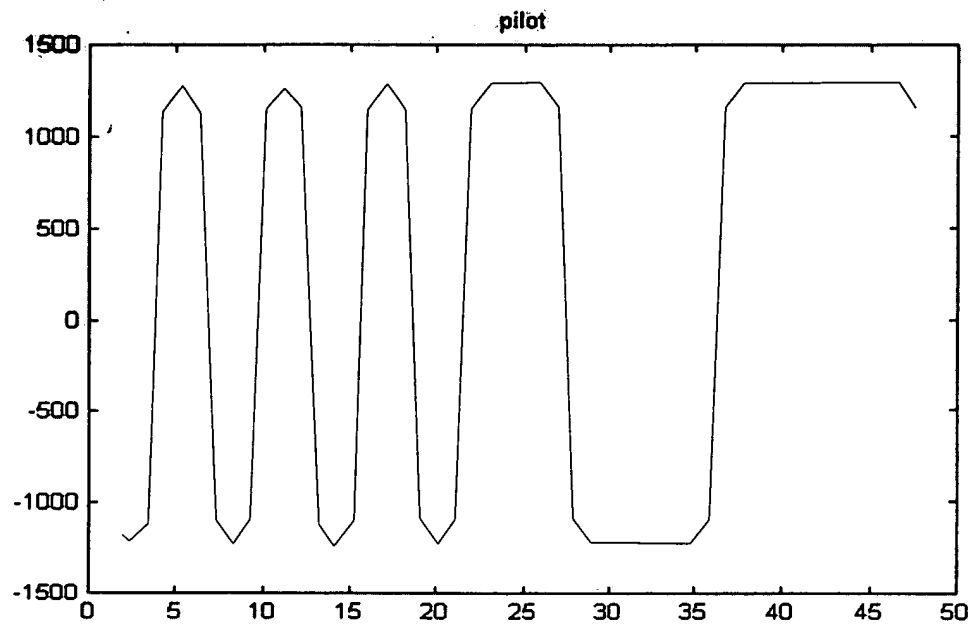


FIG. 6g

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P	P	P'		P	P	P'		P	P	P'		Sync word	data
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The timing diagram shows the clock signals for the registers in the table above. The diagram has 14 horizontal lines, each corresponding to a register. Arrows indicate clock edges. The signals are labeled with Δ , δ , and ϵ .

FIG. 7